M2X5

The EMIDEC 2400 computer: references.

Note that the background to all the early British stored-program projects from 1945 – 1951 is summarised in: *Alan Turing and his Contemporaries: building the world's first computers*. Simon Lavington [ed]. Published by BCS in 2012. 111 pages; many illustrations. ISBN: 978-1-90612-490-8.

Another general account, spanning the period from the late 1940s to the mid-1960s, is: *Early British Computers* (Simon Lavington). Published by Manchester University Press (in the UK) and Digital Press (in the USA) in 1980. 139 pages, many illustrations. ISBN: 0-7190-0803-4. Available online at: <u>http://ed-thelen.org/comp-hist/EarlyBritish.html</u>

More work is required to locate and study original documents on the EMIDEC 2400.

For a brief history of EMI's computing activities, see Section M1X5 of the *Our Computer Heritage* site, from which the following time-chart of hardware design activity is taken:

| | 1954 | 1955 | 1956 | 1957 | 1958 | 1959 | 1960 | 1961 | 1962 | 1963 |
|--------------|------|------|------|------|------|------|------|------|------|------|
| CP407, then | | | | | | | | | | |
| BMC computer | | | | | | | | | | |
| EMIDEC 1100 | | | | | | | | | | |
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| EMIDEC 2400 | | | | | | | | | | |
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| EMIDEC 3400 | | | | | | | | | | |
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. General system information on the EMIDEC 2400 is covered in the following references:

1. Ron Claydon, *Early Computer Developments at EMI*. Resurrection (the bulletin of the Computer Conservation Society), Number 16, Christmas 1996.

2. Emidec 2400 Data Processing System: general description. Undated 8-page brochure issued by EMI Electronics Ltd. Computer Division, Hayes, Middlesex. (Printer's reference code: ES17/1064/5M/360/BP).